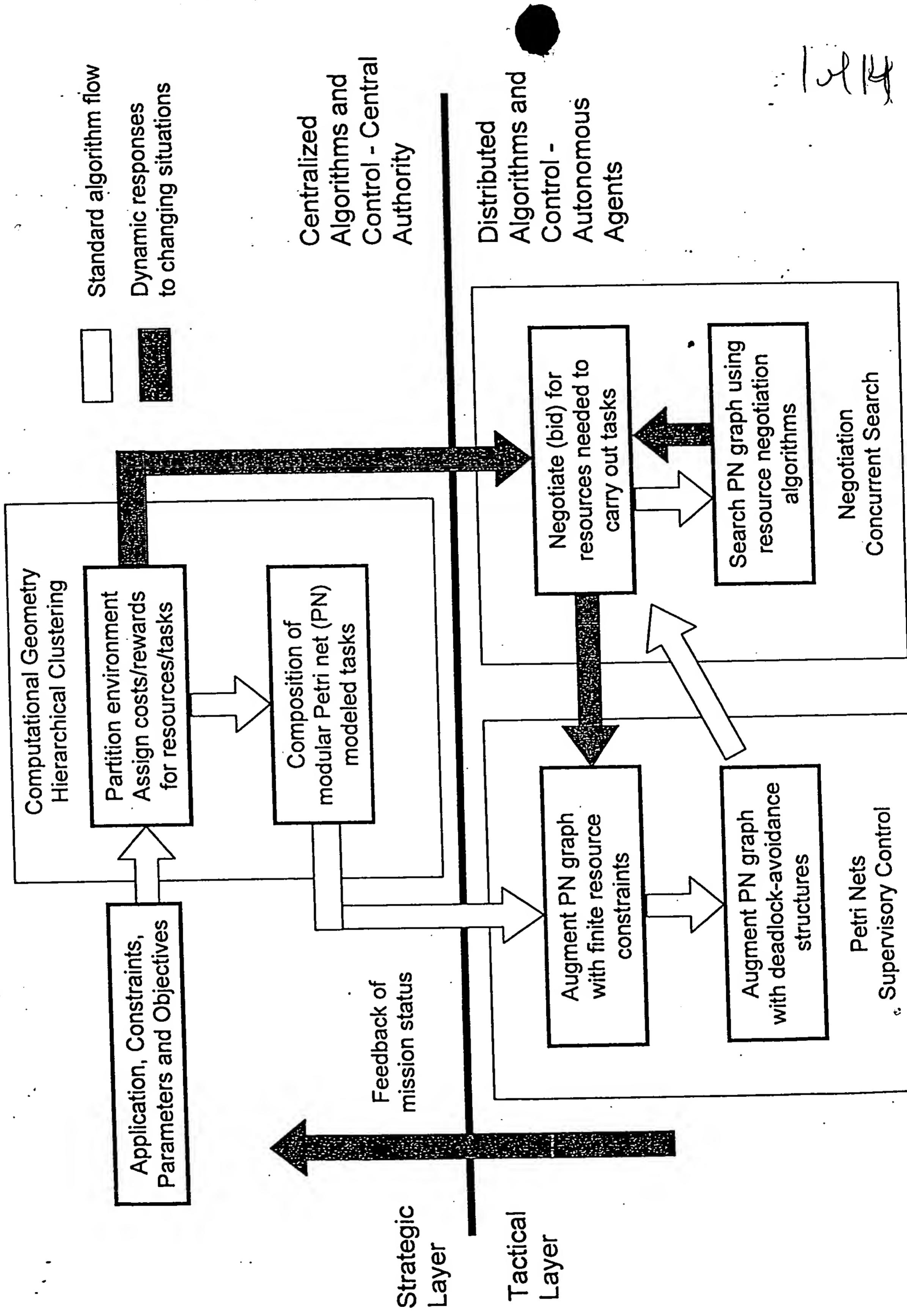


**Fig. / High Level Architecture View and Algorithm Flow**



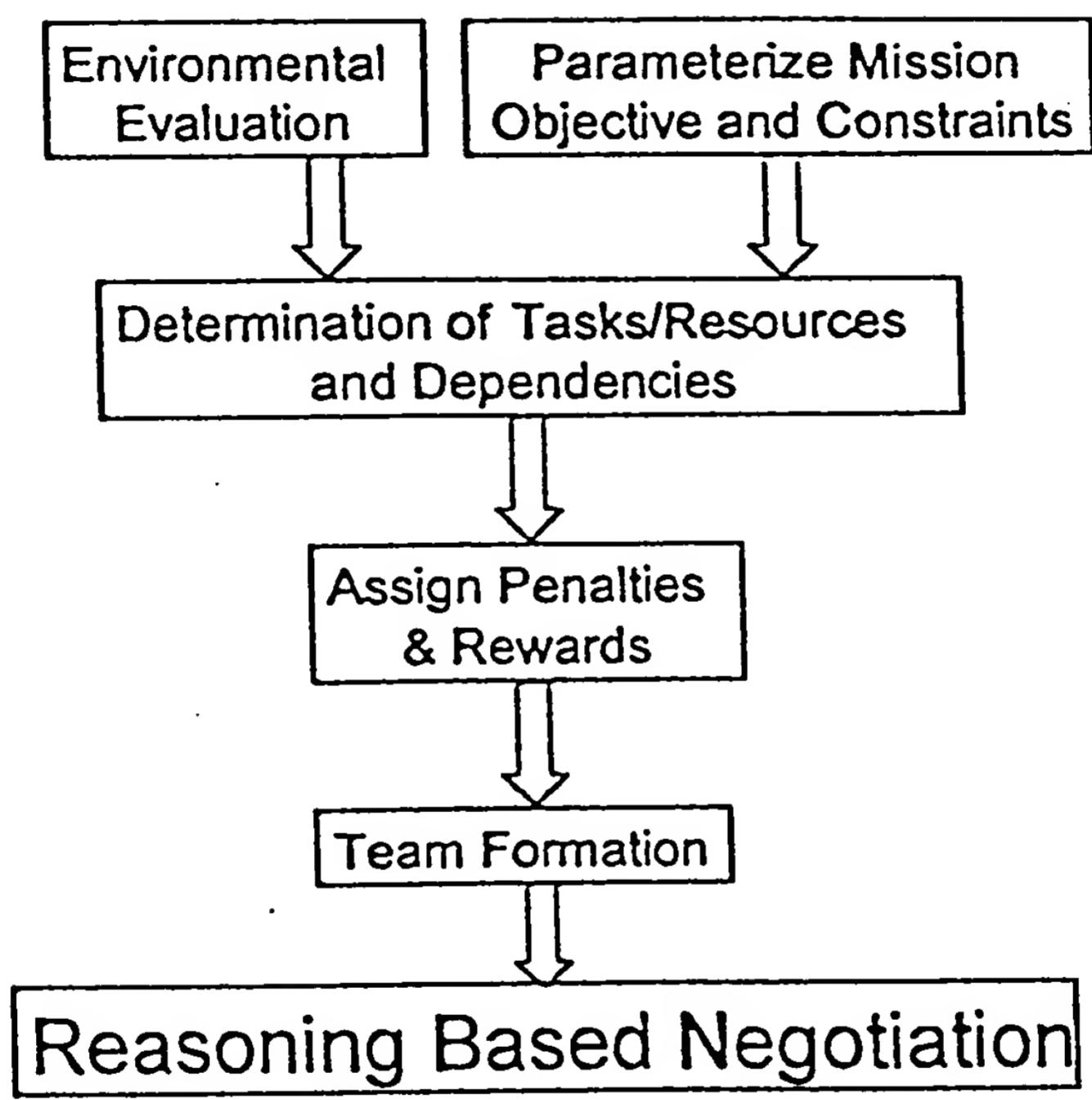


Fig 2

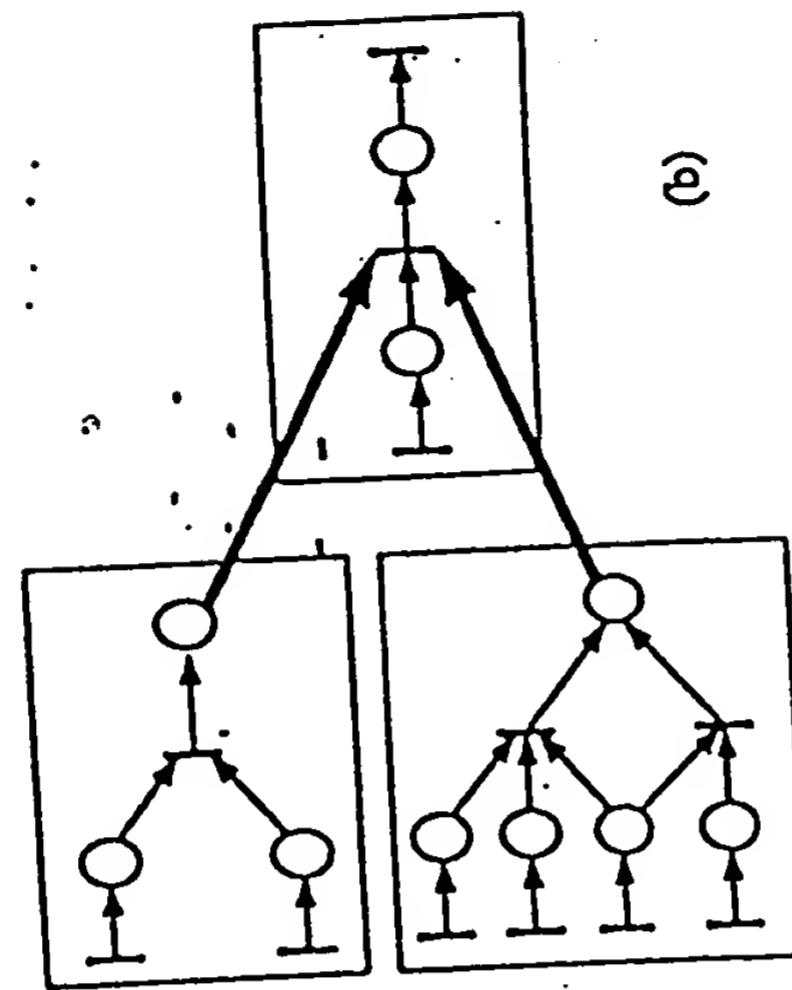


Fig 3(b)

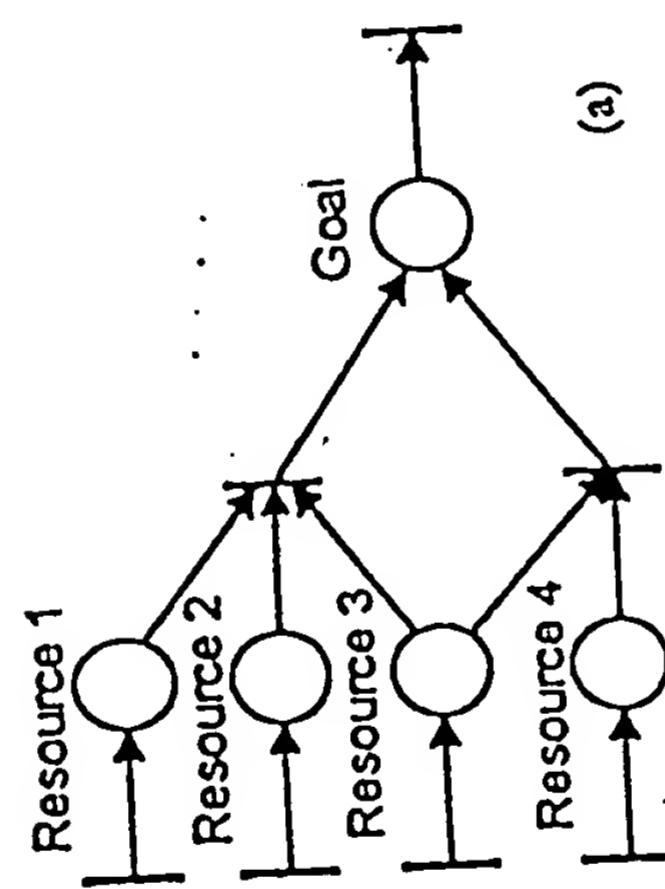


Fig 3(a)

Fig 4(b)

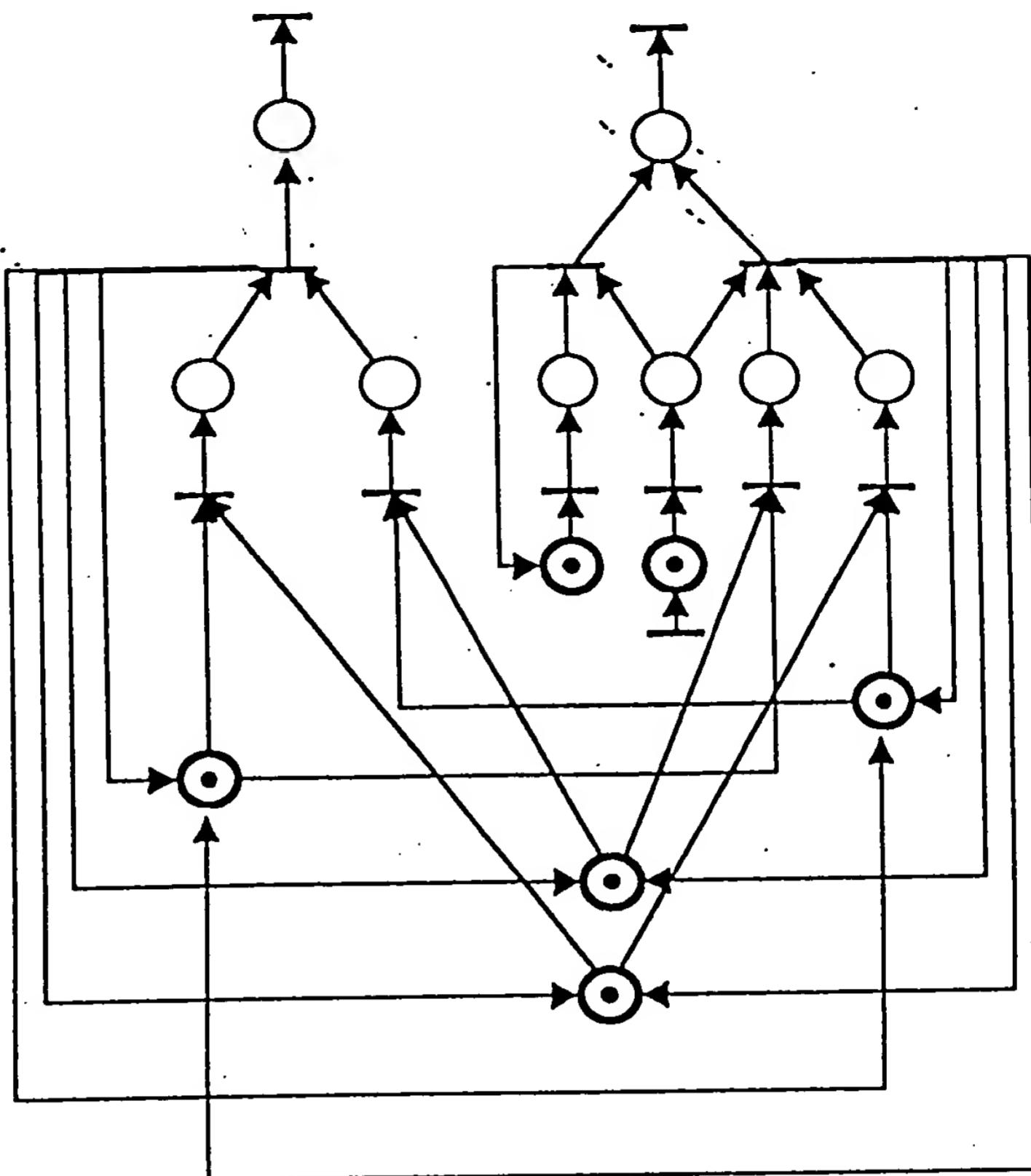
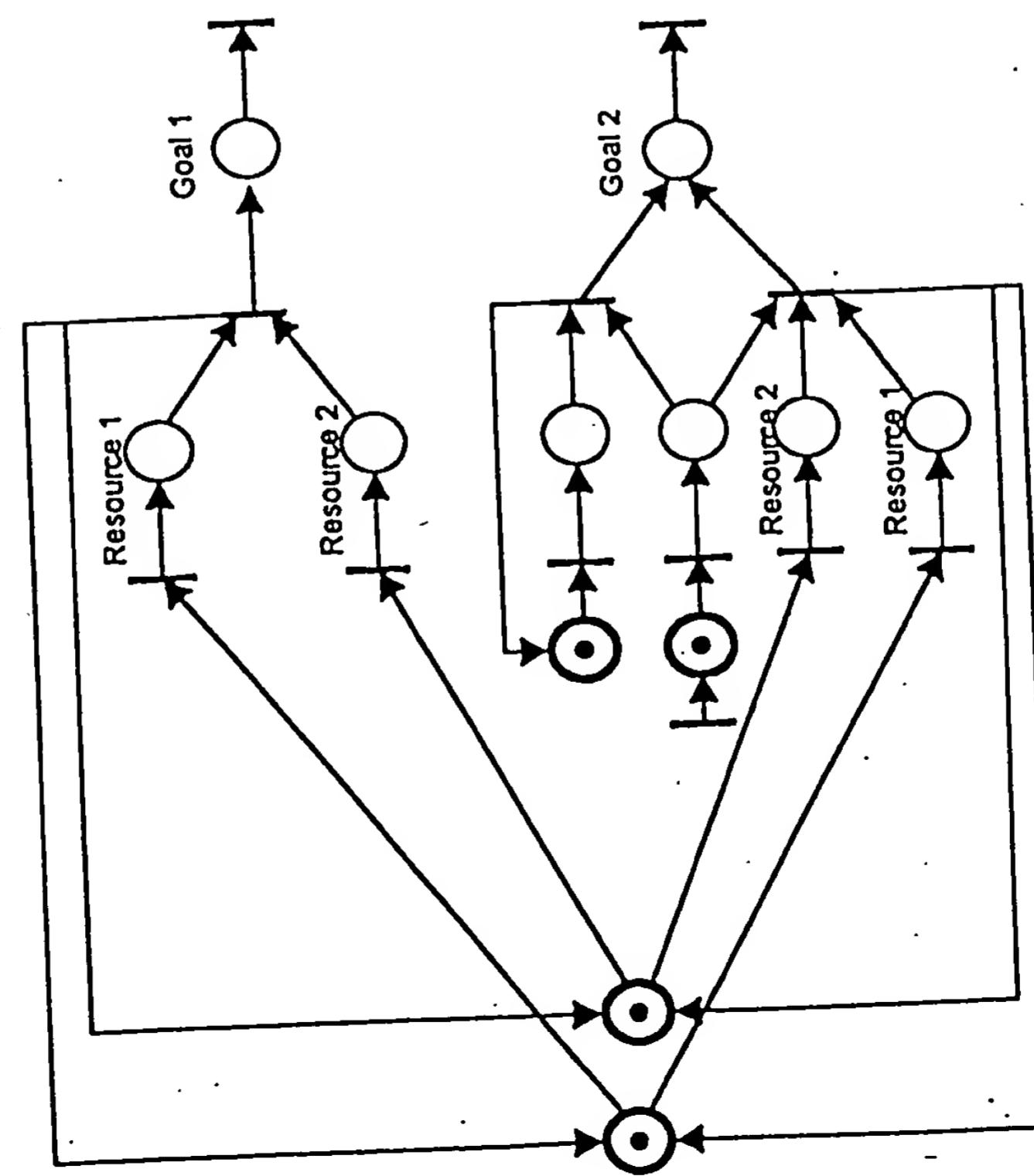


Fig 4(c)



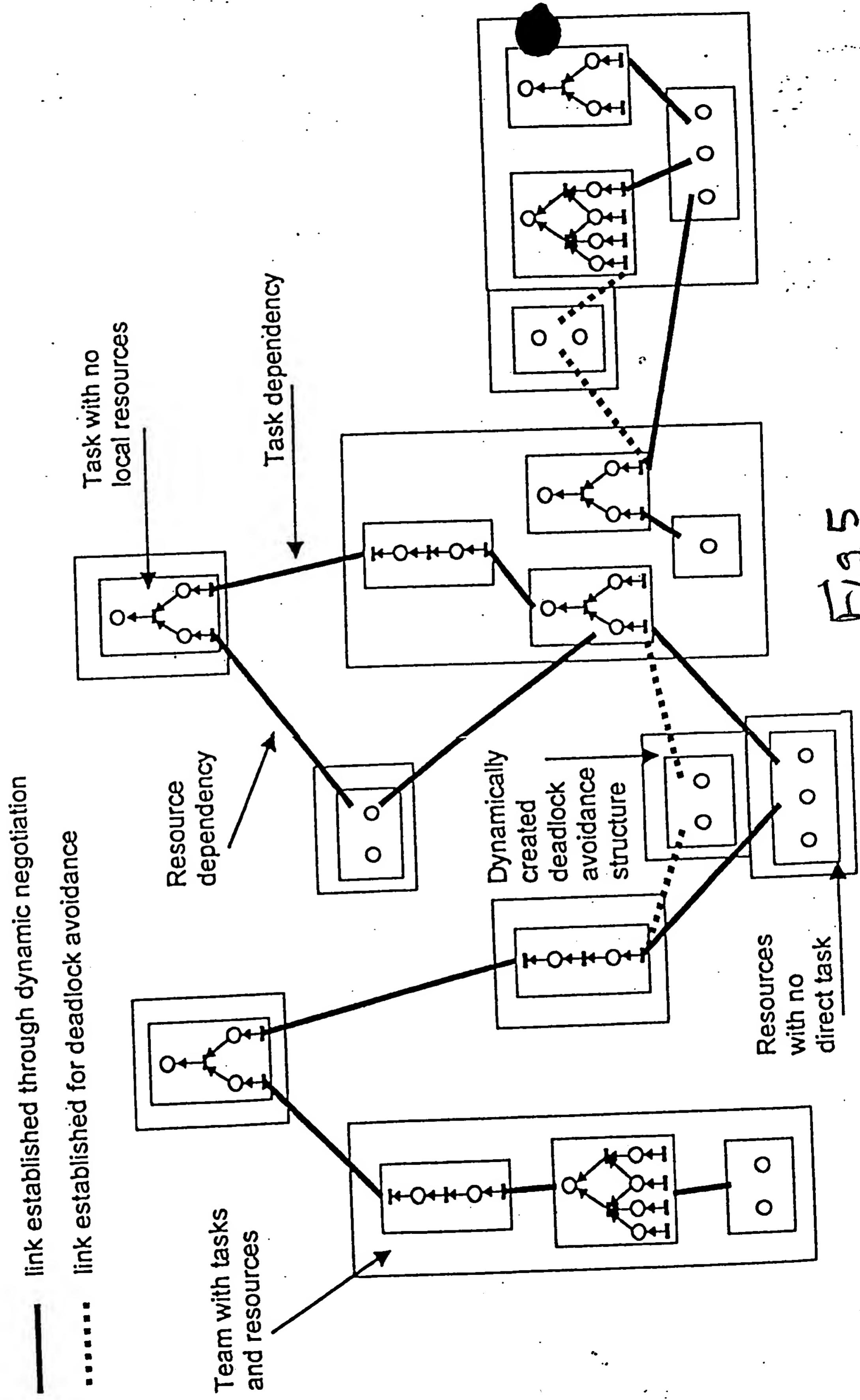
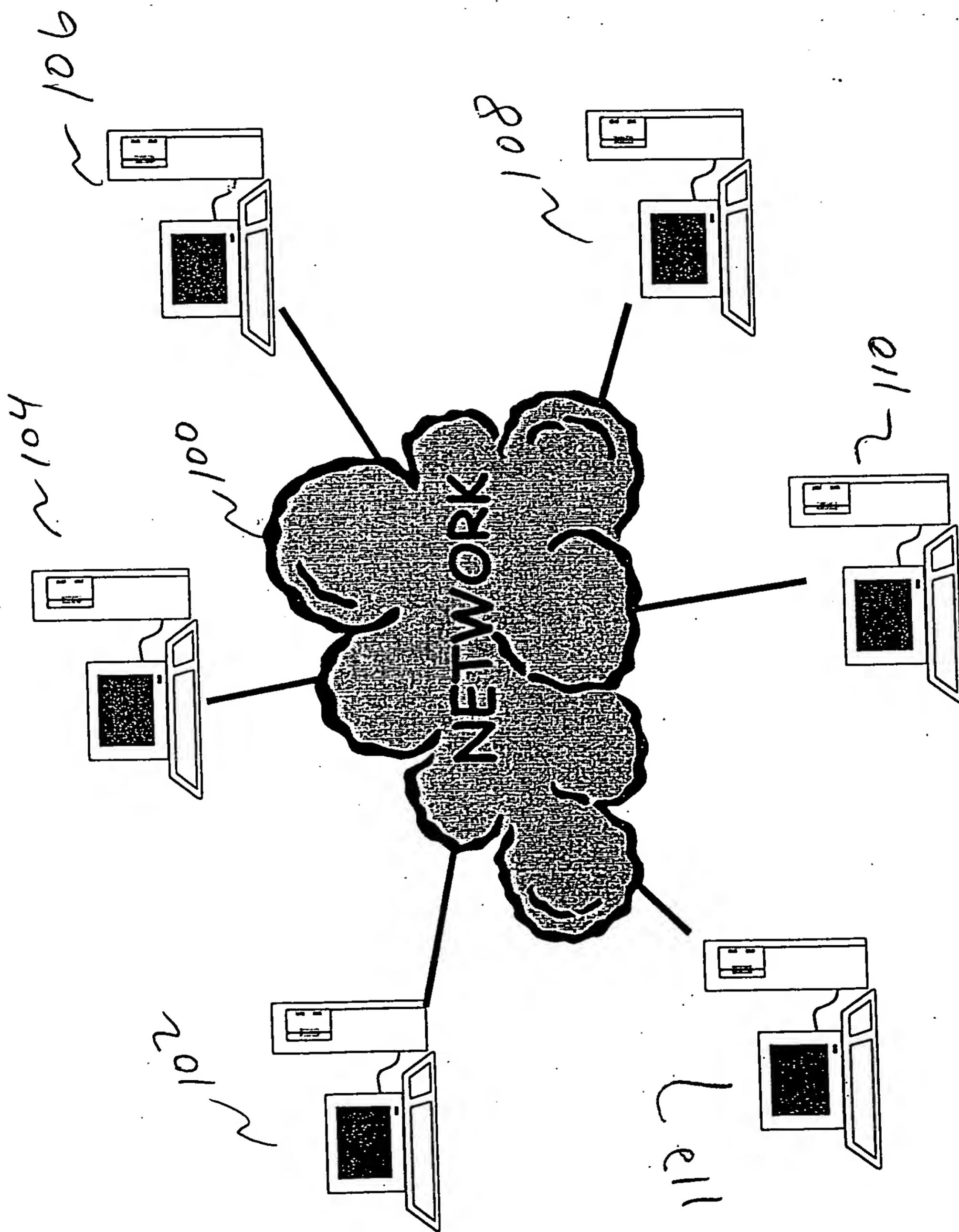
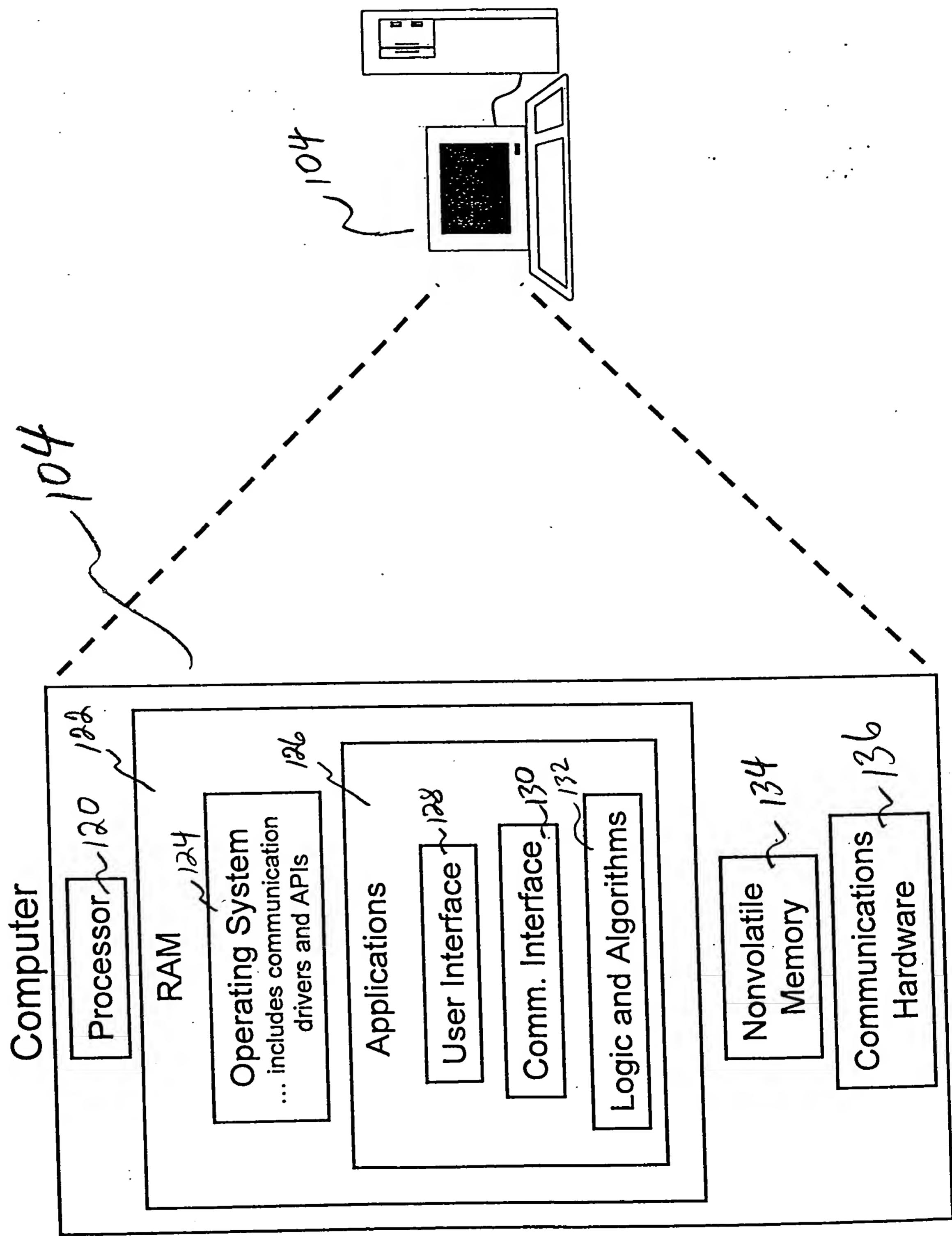


Fig 5

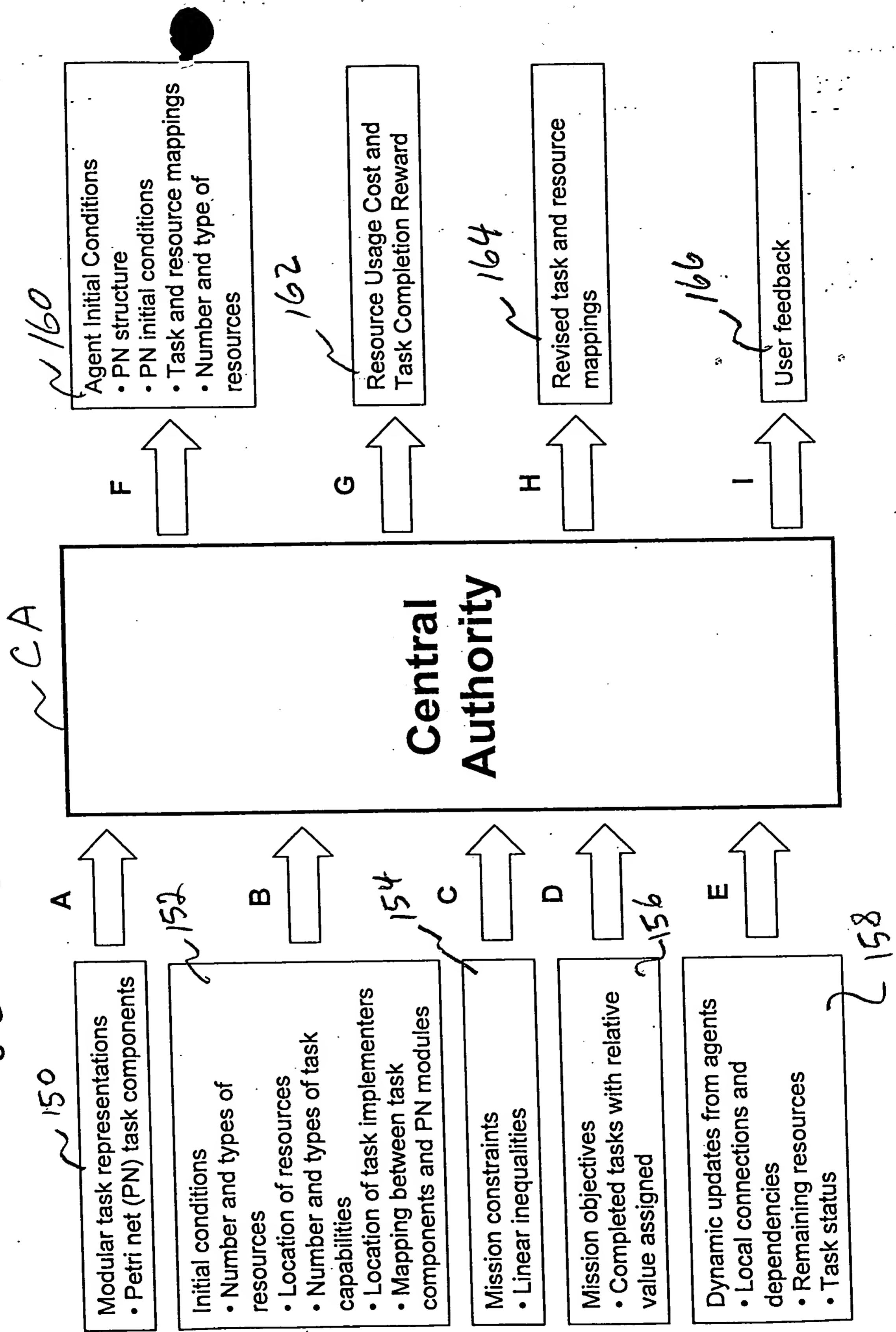
**Fig. 6 Network of central authority and agent machines**



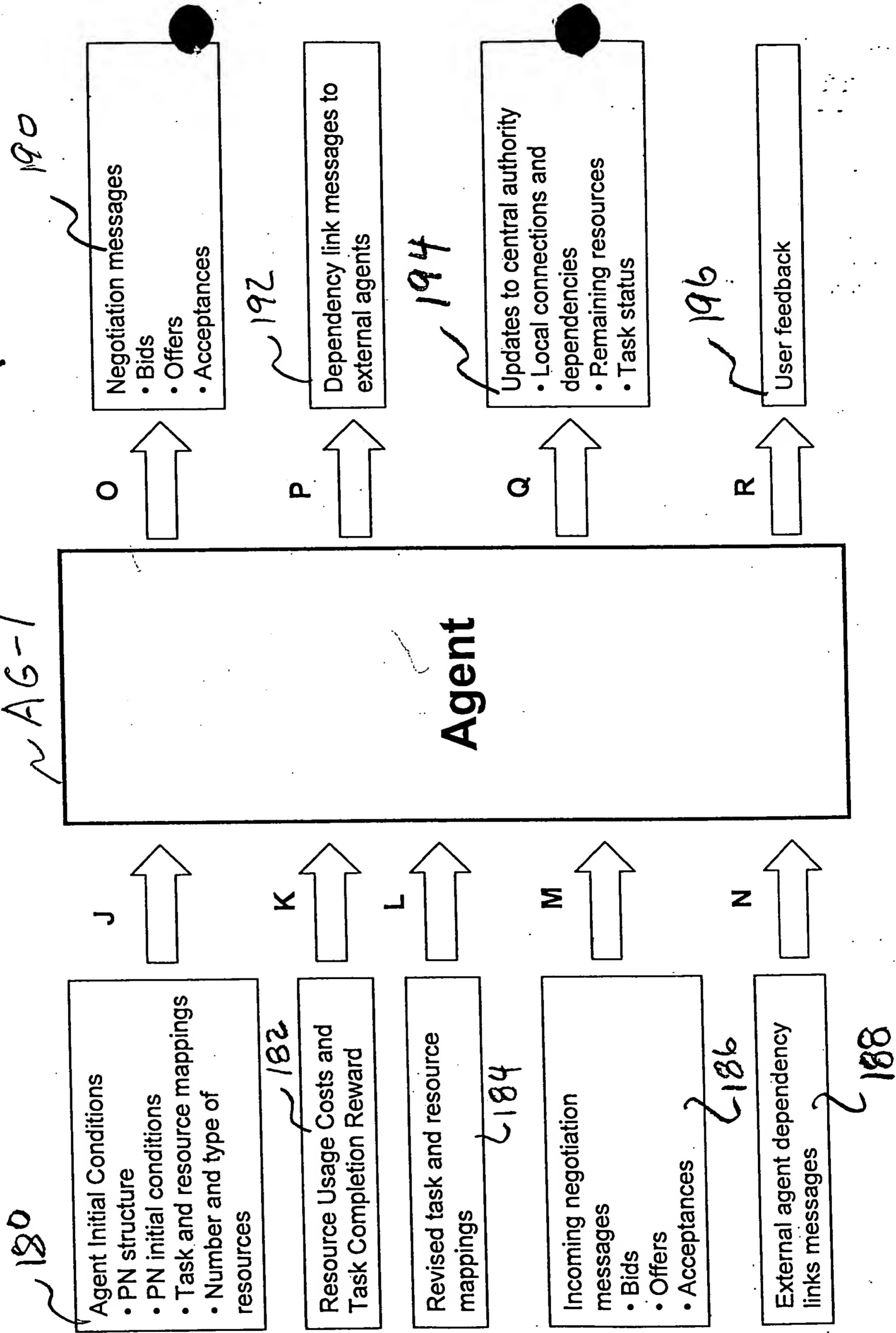
**Fig. 7 Platform hardware**



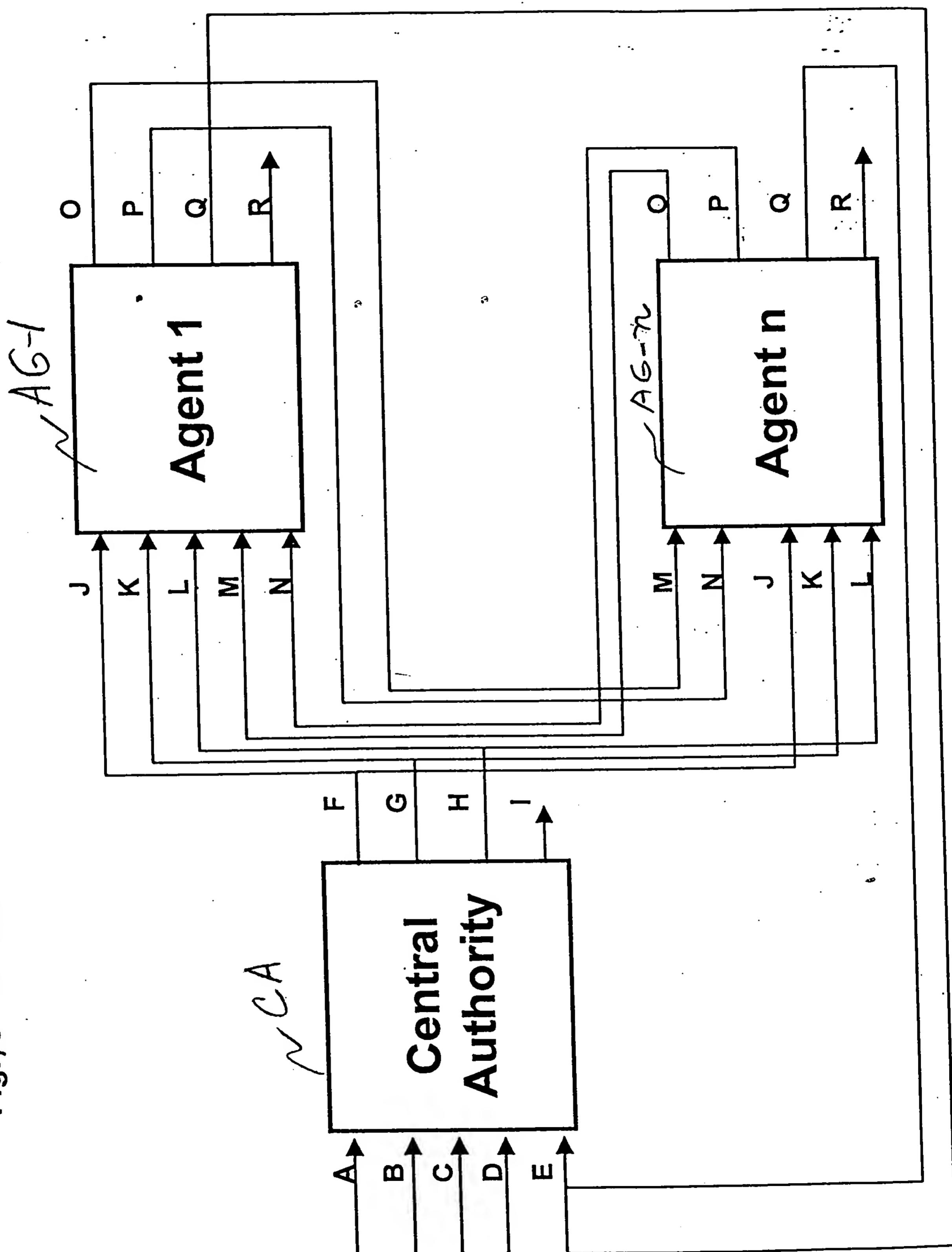
**Fig. 8 Strategic Layer / Central Authority - Inputs and Outputs**



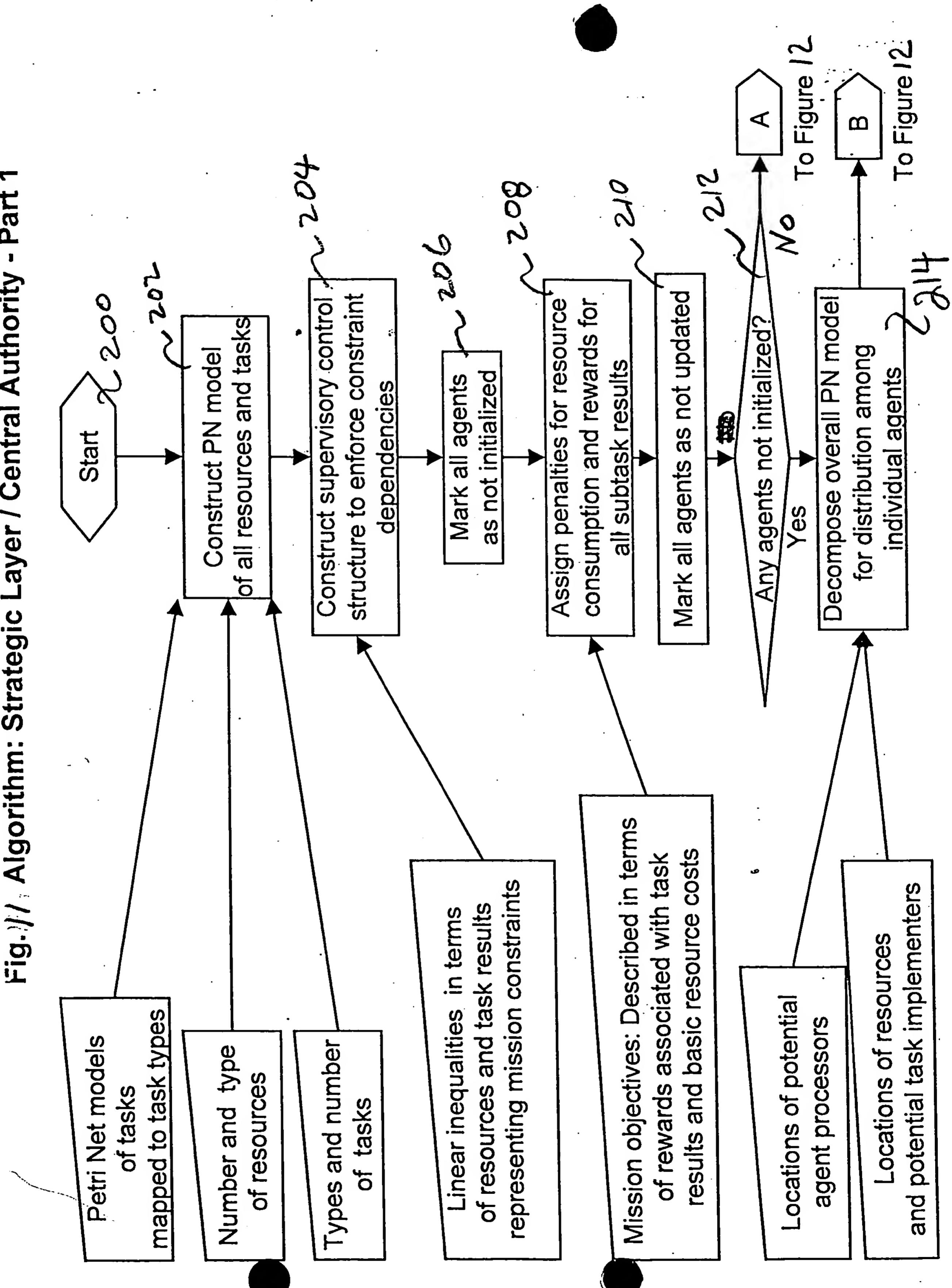
**Fig. 9** Tactical Layer / Autonomous Agents - Inputs and Outputs



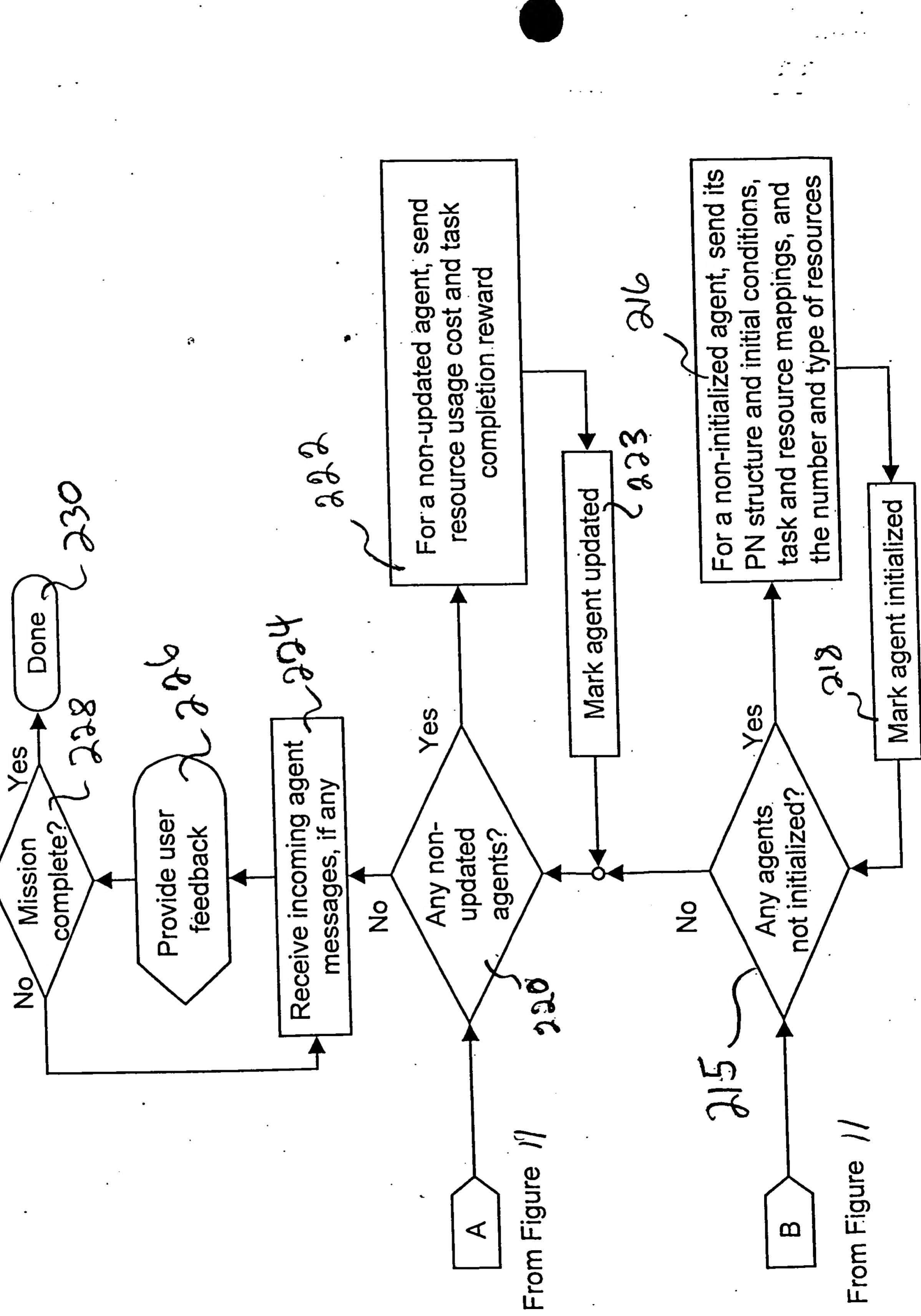
**Fig.10 Interconnections of Central Authority and Autonomous Agents**



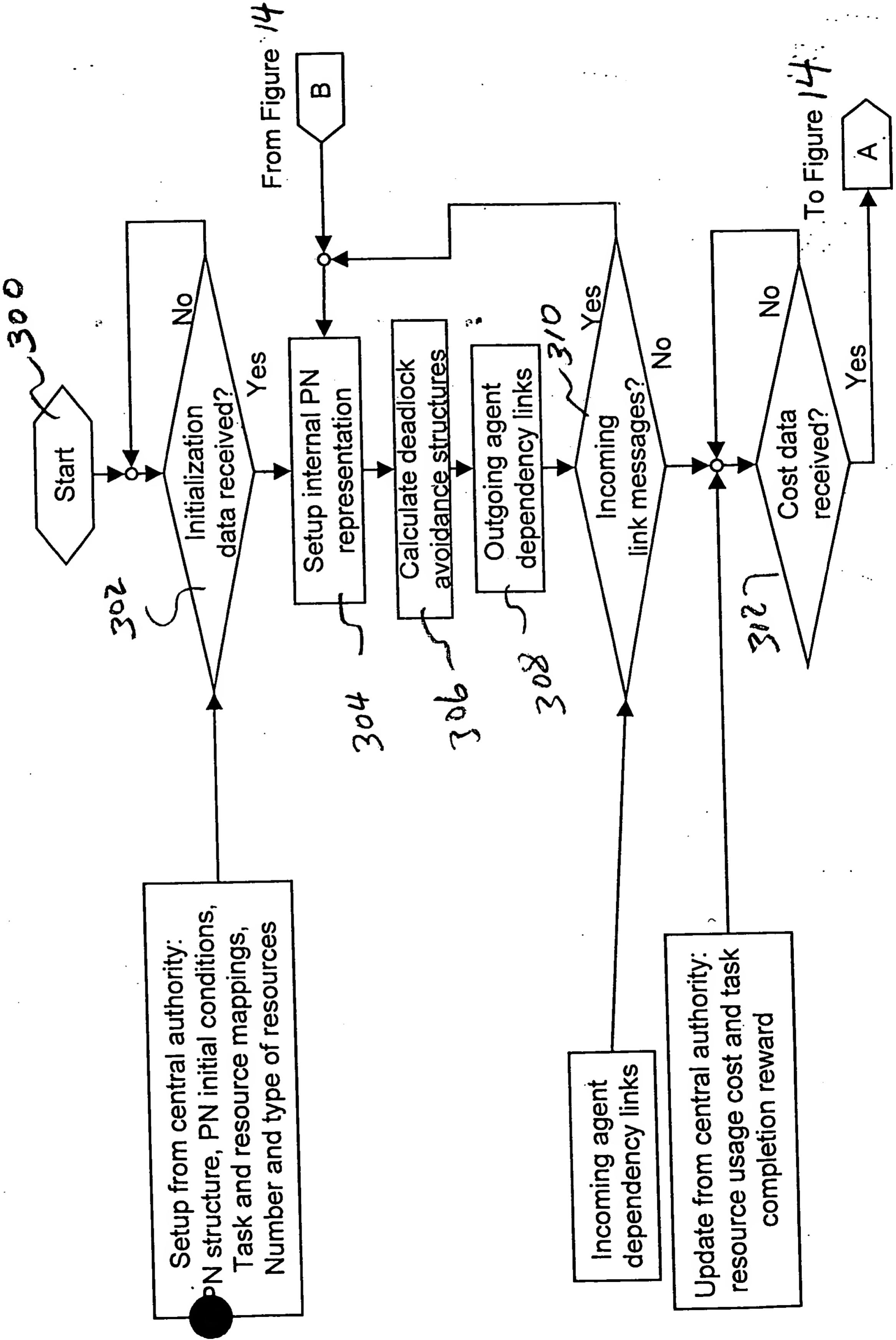
**Fig.11 Algorithm: Strategic Layer / Central Authority - Part 1**



**Fig. 12 Algorithm: Strategic Layer / Central Authority - Part 2**



**Fig.13 Algorithm: Tactical Layer / Autonomous Agents - Part 1**



**Fig. 14 - Algorithm: Tactical Layer / Autonomous Agents - Part 2**

